

#9

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Peddle, Charles I.

Group Art Unit: 2786

Serial Number: 08/909,489

Examiner: Ramesh Patel

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Title : **MEMORY MODULE ASSEMBLY USING PARTIALLY DEFECTIVE CHIPS**


ASSISTANT COMMISSIONER FOR PATENTS AND TRADEMARKS
WASHINGTON, D. C. 20231

SIR:

COMMUNICATION TO THE DRAWING PROCESSING BRANCH

Enclosed herewith is the formal drawing in the above identified application. The enclosed drawing comprises 47 sheets with figures 1A through 7N.

Respectfully,

 10/1/99
John C. Albrecht (Reg. 18,373)

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Group 2700